## Practical Malware Analysis

## The Hands-On Guide to Dissecting Malicious Software

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## errata updated to print 18

Page	Error	Correction	Print corrected
10	373e7a863a1a345c60edb9e20ec3231	373e7a863a1a345c60edb9e20ec32311	Print 2
54	Figure replacement	VWware Accelerated AMD PCNet Adapter (Microsoft's Packet Scheduler) - WireshartImage: Colspan="2">Image: Colspan="2">Image: Colspan="2">Image: Colspan="2">Image: Colspan="2">Image: Colspan="2">Image: Colspan="2">Image: Colspan="2"Image: Colspan="2">Image: Colspan="2">Image: Colspan="2"Image: Colspan="2">Image: Colspan="2"Image: Colspan="2">Image: Colspan="2"Image: Colspan="2">Image: Colspan="2">Image: Colspan="2"Image: Colspan=	Print 6

Page	Error	Correction	Print corrected
66	Figure replacement	Malware Author High-Level Language Malware Analyst Low-Level Language   int c; printf("Hello.\n"); exit(0); push ebp mov ebp, esp sub esp, 0x40   CPU Machine Code Disassembler   55 88 EC 40 55 88 EC 40   Figure 4-1: Code level examples	Print 2
74	instruction such as lea ebx, [eax*5+5], where eax is a number, rather than a memory address. This instruction is the functional equivalent of ebx = (eax+1)*5, but the former is shorter or more efficient for the compiler to use instead of a total of four instructions (for example inc eax; mov ecx, 5; mul ecx; mov ebx, eax).	instruction such as lea ebx, [eax*4+4], where eax is a number, rather than a memory address. This instruction is the functional equivalent of ebx = (eax+1)*4, but the former is shorter or more efficient for the compiler to use instead of a total of four instructions (for example inc eax; mov ecx, 4; mul ecx; mov ebx, eax).	Print 14
76	The instruction nop is actually a pseudonym for xhcg eax, eax	The instruction nop is actually a pseudonym for xchg eax, eax	Print 7

Page	Error	Correction	Print corrected
79	Figure replacement	Low Memory Address0012F004O012F0080012F002O012F0100012F010O012F0140012F018O012F015020012F012O012F0240012F022O012F0200012F024O012F0240012F028O12F0250012F026O12F0360012F036O12F0370012F036O12F0380012F036O12F0340012F036O12F0340012F036O12F035012F036O12F036012F036O12F037012F036O12F038012F036O12F036012F036O12F037012F036O12F036012F036O12F037012F036O12F036012F036O12F037012F036O12F040012F046O12F040012F046O12F040012F046O12F0405012F050	Print 8
82	This works in the same way as cmpsb, but it compares the byte located at address $ESI$ to AL, rather than to $EDI$ .	This works in the same way as cmpsb, but it compares the byte located at address EDI to AL, rather than to ESI.	Print 8
84	Table 4-12	Listing 4-2	Print 10
110, 111	<pre>printf("total = %d\n", x);</pre>	<pre>printf("Total = %d\n", x);</pre>	Print 4
111, 112	00401006 mov dword ptr [ebp-4], 0 0040100D mov dword ptr [ebp-8], 1	00401006 mov dword ptr [ebp-4], <b>1</b> 0040100D mov dword ptr [ebp-8], <b>2</b>	Print 2
148	The lpStartupInfo structure for the process stores the standard output <b>0</b> , standard input <b>0</b> , and standard error <b>6</b> that will be used for the new process.	The lpStartupInfo structure for the process stores the standard output 2, standard input 3, and standard error 1 that will be used for the new process.	Print 2
178	and 0x411001 if the language is Chinese.	and 0x41100A if the language is Chinese.	Print 7
258	<pre>CreateProcess(,"svchost.exe",,CREATE_SUSPEND,);</pre>	<pre>CreateProcess(,"svchost.exe",,CREATE_SUSPENDED,);</pre>	Print 2

Page	Error	Correction	Print corrected
263	Every thread has a queue of APCs attached to it, and these are processed when the thread is in an alertable state, such as when they call functions like WaitForSingleObjectEx, WaitForMultipleObjectsEx, and Sleep.	Every thread has a queue of APCs attached to it, and these are processed when the thread is in an alertable state, such as when they call functions like WaitForSingleObjectEx, WaitForMultipleObjectsEx, and SleepEx.	Print 2
290			Print 5
	<pre>cbuf = f.read()</pre>	<pre>cbuf = cfile.read()</pre>	
338	Figure replacement	MOV ax, 05EBh XOR eax, eax JZ -7 Fake CALL 66 B8 EB 05 31 C0 74 F9 E8 JMP 5 Real Code Figure 15-5: Multilevel inward-jumping sequence	Print 2
338	74 F9 jz short near ptr sub_4011C0+1	74 FA jz short near ptr sub_4011C0+2	Print 2
339	F9 db 0F9h	FA db 0FAh	Print 2
363	Because INT 0x2D is the way that kernel debuggers set breakpoints, the method shown in Listing 16-10 applies.	Because INT 0x2D is the way that kernel debuggers set breakpoints, the method shown in Listing 16-9 applies.	Print 2
376	0x5668 (vx)	0x5658 (vx)	Print 14
440	3. At 0x4036F0, there is a function call that takes the string Config error, followed a few instructions later by a call to CxxThrowException.	3. The function 0x4036F0 is called multiple times and each time it takes the string Config error, followed a few instructions later by a call to CxxThrowException.	Print 6
447	\WOW64	\SysWOW64	Print 12
448	C:\Windows\WOW64	C:\Windows\ <mark>Sys</mark> WOW64	Print 12
471	URL update	You can download PEview from http://wjradburn.com/software/	Print 2
499	View ► Graphs ► Xrefs From	View ▶ Graphs ▶ User Xrefs Chart	Print 2
514	If the call <b>fails</b> , the program exits.	If the call <mark>succeeds</mark> , the program exits.	Print 2

Page	Error	Correction	Print corrected
523	$\dots$ if so, it calls the Sleep function to sleep for <b>60</b> seconds.	if so, it calls the Sleep function to sleep for about 394 seconds.	Print 6
533	If you perform a full analysis of $0x402520$	If you perform a full analysis of $0x402510$	Print 7
649	The two functions (sub_4012F2 and sub_401369)	The two functions (sub_40130F and sub_401386)	Print 2
675	The malware is querying the I/O communication port (0x5668)	The malware is querying the I/O communication port (0x5658)	Print 14
680	as described in "Searching for Vulnerable Instructions" on page 67 <mark>0</mark> .	as described in "Searching for Vulnerable Instructions" on page 678.	Print 6